

Manual Update Sheet

DATE: February 29, 2000

Document Being Updated: *TMS320C6000 CPU and Instruction Set Reference Guide*

Literature Number Being Updated: SPRU189E

Manual Included in a Kit: No

This Manual Update Sheet (SPRZ168) ships with the *TMS320C6000 CPU and Instruction Set Reference Guide*.

Page:	Change or Add:
3-111	<p>In the SHL instruction, Description section, replace the last paragraph with the following:</p> <p>If $39 < src1 < 64$, <i>src2</i> is shifted to the right by 40. Only the six LSBs are used by the shifter, so any bits set above bit 5 do not affect execution.</p>
3-113	<p>In the SHR instruction, Description section, replace the last paragraph with the following:</p> <p>If $39 < src1 < 64$, <i>src2</i> is shifted to the right by 40. Only the six LSBs are used by the shifter, so any bits set above bit 5 do not affect execution.</p>
3-115	<p>In the SHRU instruction Description section, replace the last paragraph with the following:</p> <p>If $39 < src1 < 64$, <i>src2</i> is shifted to the right by 40. Only the six LSBs are used by the shifter, so any bits set above bit 5 do not affect execution.</p>
5-33	<p>In the ADDKPC instruction, Syntax section, change the following:</p> <p>From <code>.unit = .S1, .S2</code> to <code>.unit = .S2</code>, and in the Opcode table from Unit <code>.S1, .S2</code> to Unit <code>.S2</code>.</p>